ABSTRACT

A redundant memory architecture having an active memory and an inactive memory. The active memory supports in-service storage operations. The inactive memory is updated with stored contents of the active memory. Stored contents of the inactive memory are defragmented prior to an activity switch that results thenceforth in the inactive memory assuming the in-service storage operations and the active memory being updated with the stored contents of the inactive memory. The redundant memory architecture further has a data structure to maintain correspondence between the stored contents of the active memory and those of the inactive memory.

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